News from HCs, ICs,TGs and SIGs

Privileged Spec IC

Ssqosid - QoS ID register

The QoS Identifiers extension (Ssqosid), initiated by the Capacity and Bandwidth controller QoS Register Interface (CBQRI) Task Group, has been approved for Fast-Track development by AR. It's now advancing to the subsequent development stages.

- View the Specification Source: riscv-ssqosid
- Track its Development: RVS-1687 on Jira

Unprivileged Spec IC

Zabha - Byte and halfword atomics

The Zabha extension, which introduces byte and halfword atomic memory operations, has been approved for Fast-Track development by AR. This extension complements the A and Zacas extensions. It's now moving to the next stages of development.

- View the Specification Source: riscv-zabha
- Track its Development: RVS-1685 on Jira

SOC Infrastructure HC

RAS Error-record register interface (RERI) specification for internal review

The RAS (Reliability, Availability, and Serviceability) Error-record Register Interface (RERI) Task Group has finalized its stable specification for internal member review. We invite you to read the document and join the ensuing discussion to provide valuable insights before we finalize the spec. Your feedback is crucial at this stage.

News From the CTO Office

Elections

We are excited to share some updates regarding the TSC representative roles:

- **Ken Dockser** of Tenstorrent and **Roger Espasa** of Semidynamics have been elected as the new TSC Strategic Member representatives.
- **Guy Lemieux**, an Individual Member, has taken the role of TSC Community/Individual representative.

Please join us in congratulating Ken, Roger and Guy on their new positions!

Calendar in your own timezone

The RISC-V Technical calendar is now automatically available in your own timezone. To view the calendar in your own timezone, click on the link.

Etherpad

We're piloting Etherpad to enhance our note-taking during meetings. Etherpad is a real-time, browser-based editor where users can co-edit documents instantly. No login needed – simply create a pad and begin. Afterwards, export your notes to platforms like GitHub or Google Docs. Etherpad should not store permanent content. To access Etherpad, click on the link. Pads are reusable, so feel free to use the same pad for multiple meetings.

Jira and GitHub Access

To ensure a seamless experience with RISC-V International's tools and platforms, please first verify your access to Groups.io, GitHub, and Jira by clicking on the following Verify Access.

If you encounter any issues or do not have the necessary permissions, kindly reach out to us by sending an email to help@riscv.org.

Upcoming Events

- **RISC-V Summit North America**: November 7-8, 2023, Santa Clara, CA, USA. Schedule is live! Register today.

Newsletter Contributions

To submit a topic for our newsletter, click here. Be sure to carefully read the submission guidelines!

Contact Information

For any queries, visit our help.riscv.org or email us at help@riscv.org.

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