Technical Newsletter
2024-01-12
Volume: 8

News from TSC, ARC, ICs, HCs, SIGs and TGs

Profiles & Platforms

Transition from Profiles TG to Profiles SIG

The existing Profiles TG is going to wind down, and will soon be replaced by a new Profiles SIG with a broader SIG-oriented charter that encompasses broad profile-related topics, including ones that had started to be discussed in the TG. You can find more details about this change in the recent announcements made by Greg Favor (here and here), Chair of the Technical Steering Committee.

Applications and Tools HC

AI/ML Applications SIG Call for Participation

We invite you to join and contribute to the AI/ML Applications SIG. To participate, simply click the "Join This Group" button at the bottom of the https://lists.riscv.org/g/sig-ml-ai-apps page. The announcement with more details can be found here.

ISA Infrastructure HC

Documentation Task Group

Full integration of the ISA Specifications is progressing. Pull Requests have been submitted for Bitmanip and Vector for integration into the Unprivileged Specification and await final review for merging. Similarly, all ratified ISA specifications are queued for addition to their appropriate location in either the Unpriv or Priv specs. Conversion of standalone ISA and non-ISA specifications, such as Debug and Trace to AsciiDoc has been completed and is in the final stages of review. Look for announcements soon of new releases of the AsciiDoc versions of all previously ratified specifications.

If you're interested in the tooling and/or guidelines for all RISC-V documentation be sure to subscribe

to the Documentation SIG mailing list. Discussions around topics such as accessibility (for vision impaired readers), automated diagramming, a style guide/linting, etc. have been occurring and more participation is always welcome.

News from the CTO Office

> *Groups.io Profile Update*
>
> **IMPORTANT**
>
> Please update your Groups.io Profile with your Display Name, GitHub ID, and Affiliation. For those who require a Google-enabled email address to access the RISC-V Google Drive, and whose main email on Groups.io is incompatible, the option to set an alternative Google-enabled email is available (if you need to set this alternative email, please contact help@riscv.org).
>
> To update your profile, click here.

Call for Candidates

- **Call for Chair/Vice-chair Candidates for the CHERI TG**. The call is open until **January 19, 2024** and can be found here.
- **Call for Chair/Vice-chair Candidates for the External Debug Security TG**. The call is open until **January 15, 2024** (a.k.a. next Monday) and can be found here.

If you want to nominate yourself or someone else for any of the open positions, please email **help@riscv.org**.

Votes

TSC

- TSC Approval Vote to send the Indirect CSR Access Fast Track Extension to the Board for Ratification (Voting). Voting closes on 2024-01-26. Jira: RVS-1250.

HC & IC Committees

- Freeze Milestone for QoS Ids Fast Track and the CBQRI non-ISA Specification Committee Chair Sign Off (Voting). Voting closes on 2024-01-26. Jiras: RVS-753 and RVS-1982.
- Freeze Milestone for Byte and Half-word Atomic Memory Ops Fast Track Extension Committee Chair Sign Off (Draft). Jira: RVS-1685.

Technical Resources

Active ICs, HCs, SIGs and TGs

You can find the list of active ICs, HCs, SIGs and TGs at tech.riscv.org/groups. If you find any incosistencies, please report it via help@riscv.org or help.riscv.org. A similar page for all Extensions is under development.

Jira@RISC-V

As we continue to grow, we are working to improve our tools and processes to better serve the community. We are moving to Jira to manage the specification development process and groups lifecycle. The Jira@RISC-V instance is available at jira.riscv.org. We are currently in the process of migrating the existing issues to Jira but would like to invite you to access it using your Linux Foundation account. No other action is required at this time.

Tools Access

You can verify your access the current set of tools used within RISC-V International at tech.riscv.org. If you find any incosistencies, please contact help@riscv.org.

Technical Meetings Calendar Adjusted to Your Local Time Zone

The **RISC-V Technical Meetings Calendar** is available in your own timezone here.

Etherpad

Etherpad, a real-time browser-based editor where users can co-edit documents instantly, is available. No login needed. Etherpad should not store permanent content. To access Etherpad, click on the here. Pads are reusable, so feel free to use the same pad for multiple meetings.

Upcoming Events

RISC-V Summit Europe 2024

- HiPEAC 2024: January 17-29, 2024, Munich, Germany.

- Open Source Summit North America: April 16-18, 2024, Seattle, Washington.

- RISC-V Summit Europe: June 24-28, 2024, Munich, Germany.

Glossary

- **TSC**: Technical Steering Committee
- **ARC**: Architectural Review Committee
- **IC**: ISA Committee
- **HC**: Horizontal Committee
- **SIG**: Special Interest Group
- **TG**: Task Group

Newsletter Contributions

To submit a topic for the Technical Newsletter, click here. Be sure to carefully read the submission guidelines!

Contact Information

For any queries, visit help.riscv.org or email us at help@riscv.org.

riscv.org