# RISC-V Tech

## Welcome to the RISC-V Technical Wiki

### Learn

- **Start Here - Getting Started Guide**: [English] | [Chinese]
- **See RISC-V Groups**: [Org Chart]
- **Find Ratified Specifications**: [List]
- **Review RISC-V Technical Policies**: [Approved] | [All]
- **Understand the RISC-V GitHub Organization**: [Overview]
- **Locate RISC-V Education**: [Courses] | [GitHub]
- **Watch Technical Sessions**: [2024] | [2023]
- **Peruse RISC-V collaboration documents**: [Google Drive]
- **Read the RISC-V Specification States**: [Definitions]
- **Explore Sail - Tutorial**: [Video] & [Source] | [Add New Extension]

### Engage

- **Dive Into Groups & Specification Development - Lifecycle Guide**: [English] | [Chinese]
- **Understand Community Norms**: [RISC-V Code of Conduct]
- **Attend Current Tech Meetings**: [Technical Calendar]
- **Join Mailing Lists, Read Archives**: [Mailing Lists]
- **Find Group Working Documents**: [GitHub] | [Google Drive]
- **Participate in Specification and Group Development**: [Highlights]
- **Lead/Host/Join Meetings**: [Technical Meetings Primer] | [Meeting Disclosures]
- **Develop Sail Code**: [Golden Model SIG group] | [RISC-V Model] | [Cookbook]
- **Contribute to ACTs**: [Architecture Test SIG group] | [riscv-arch-test repo]
- **Engage in Ecosystem**: [DevPartners] | [DevBoards] | [Labs]

### Stay Up to Date

- **Latest Top of the Tree (main branch)**: Specification ISA
- **Software Ecosystem Dashboard**
- **Ratified Extensions**
- **Active Groups (ICs, HCs, SIGs and TGs)**
- **Active Specification Status**
- **Certification Steering Committee (CSC)**
- **Voting Status**
- **Technical Newsletter**
- **RISC-V News**

### More...

If you are looking for the old Home page, please see the [Home - Classic page](#).