

Vector Crypto Standardization Status Summary

Overall Approved Definition-of-Done Status: Kick-Off

Next Major Definition-of-Done Milestone: Stable

- [Ratification plan](#)
- [Status checklist](#)

Presentations

- [Proposed Vector Crypto Instructions](#)
- [Vector Crypto Questions](#)

Specification

- Latest pdf from AsciiDoc:

[Vector Crypto Frozen Specification 1.0.0-rc1](#)

- The latest pdf can always be found from the <https://github.com/riscv/riscv-crypto> page. Just go to the box that says "Releases" and click on the Release with the "Latest" label.
- You can get to [Releases](#) directly

OpCode Encoding/OpCode consistency review

Architecture Tests

ABI Extensions

GCC and Assembler

- (Intrinsics)

(links to GitHub, etc., as relevant to each of the topics here)

(links to PLCT and IIT Madras status pages, TBD)

LLVM

SAIL

Spike

QEMU

Proof-of-Concept

[Verilog file implementing the GHASH algorithm \(v2\)](#) Donated by Barry Spinney (Nvidia)

[PPT presentation on implementing the GHASH algorithm](#) Barry Spinney