Tools

This page gives an overview of upstream projects. If you miss information or find mistakes, please edit.

- OpenOCD
  - RISC-V status
- Valgrind

OpenOCD

OpenOCD is an open-source tool for on-chip debugging, in-system programming and boundary-scan testing.

- OpenOCD Homepage
- OpenOCD source code

RISC-V status

Tim Newsom (SiFive) is maintaining a RISC-V port, which is mainly tested with SiFive cores.

Lack of time and review starvation led to a big amount of patches, which are not mainlined yet.

Tim is welcoming pull requests for new features, cleanups, etc.

The port can be found here:

- https://github.com/riscv/riscv-openocd

Valgrind

Valgrind is an instrumentation framework for dynamic analysis tools.

- Valgrind Homepage
- Git Repository