RISC-V Active ICs, HCs, SIGs and TGs

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	Status							
Group Type	NOT STARTED	DONE	INCEPTION	PENI	Sumi	mary	Updated	Status
Gover nance Commi	0	0	0	0	RVA2 /RVB: Profile	23	May 08, 2024	ACTIVE
ttee (GC)					Memo Taggi		May 07, 2024	INCEPTIO
Horizo ntal Commi	0	0	0	0	Privile Spec		May 07, 2024	ACTIVE
ttee (HC)					Secu	rity (HC)	May 07, 2024	ACTIVE
ISA Commi ttee (IC)	0	0	0	0	Palett		May 02, 2024	TODO
Specia					Shov	ving 5 out	of 124 issues	
Interes t Group (SIG)	0	0	1	3		32	0	36
Task Group (TG)	0	0	3	7		31	1	42
Irrelev ant	5	2	0	0		0	0	7
Total Uniqu e Issues :	5	2	4	10		74	1	96

Active RISC-V ISA Committees (ICs)

Summary	Updated	Status	Chair	Chair Affiliation	Vice-Chair	Vice-chair Affiliation	Group Creation Date
Unprivileged Spec (IC)	May 02, 2024	ACTIVE	Krste Asanovi	SiFive	Earl Killian	Aril Inc.	Apr 06, 2020
Privileged Spec (IC)	May 07, 2024	ACTIVE	Greg Favor	Ventana Micro Systems	Andrew Waterman	SiFive	Jul 07, 2021

2 issues

Summary	Updated	Status	Chair	Chair Affiliation	Vice- Chair	Vice-chair Affiliation	Group Creation Date
Committee Chairs	Nov 17, 2023	ACTIVE					
Task Group Chairs and Vice- Chairs	Nov 16, 2023	ACTIVE					
Technical Steering Committee (TSC)	Mar 19, 2024	ACTIVE	Greg Favor	Ventana Micro Systems	Philipp Tomsich	VRULL	Dec 05, 2020

3 issues

Active RISC-V Horizontal Committees (HCs)

Summary	Updated	Status	Chair	Chair Affiliation	Vice- Chair	Vice-chair Affiliation	Group Creation Date
Applications & Tools (HC)	Mar 20, 2024	ACTIVE	Philipp Tomsich	VRULL	David Chen	Alibaba	Dec 17, 2019
ISA Infrastructure (HC)	Jan 09, 2024	ACTIVE	Wei Wu	ISCAS	Allen Baum	Esperanto Technologies Inc.	Feb 24, 2022
Privileged Software (HC)	Apr 11, 2024	ACTIVE	Anup Patel	Ventana Micro Systems	John Hengeveld	Intel	Jun 06, 2022
SOC Infrastructure (HC)	Apr 11, 2024	ACTIVE	Vedvyas Shanbhogue	Rivos Inc.	Gajinder Panesar	Individual Member	Feb 08, 2022
Security (HC)	May 07, 2024	ACTIVE	Andrew Dellow	Huawei	Ravi Sahita	Rivos Inc.	Dec 18, 2019
Technology (HC)	Jan 09, 2024	ACTIVE	John D. Leidel	Tactical Computing Laboratories	Nambi Ju	Lyle Technologies	Apr 28, 2021

6 issues

Active RISC-V Special Interest Groups (SIGs)

Summary	Updated	Status	Chair	Chair Affiliation	Vice- Chair	Vice-chair Affiliation	Group Creation
					Gilali	Allillation	Date
Al/ML	Apr 24, 2024	ACTIVE	Tao Xie	Beijing Open Source Chip Research Institute	David Chen	Alibaba	Mar 01, 2021
Android	Apr 08, 2024	ACTIVE	Han Mao	Alibaba	Elliott Hughes	Google	May 20, 2021
Architecture Test	Apr 12, 2024	ACTIVE	Qinghao (James) Shi	Alibaba	Umer Shahid	10X Engineers	Dec 17, 2019
Automotive	Jan 09, 2024	ACTIVE	Peter Lewin	Imagination Technologies	Andy Frame	SiFive	Sept 06, 2022
Blockchain	Jan 09, 2024	ACTIVE	Patty Tu	Shanghai Wanxiang Blockchain Inc.	Gary Xu	Aitos	Apr 05, 2021

CHERI SIG	Jan 09, 2024	ACTIVE	Alex Richardson	Google	Simon Moore	University of Cambridge	Nov 23, 2022
Control Flow Integrity	Jan 09, 2024	ACTIVE	George Christou	FORTH	Vedvyas Shanbhogu e	Rivos Inc.	Oct 21, 2021
Datacenter	Jan 09, 2024	ACTIVE	Michael Wong	Codeplay	Jing Yang	Alibaba	Aug 18, 2021
Debug, Trace, and Performance Monitoring (DTPM)	Apr 12, 2024	ACTIVE	lain Robertson	Siemens	Paul Donahue	Ventana Micro Systems	Mar 20, 2023
Documentation	Apr 15, 2024	ACTIVE	Kevin Broch	Rivos Inc.			Apr 10, 2024
Embedded	Jan 09, 2024	ACTIVE	Ligang Zhang	Alibaba			Aug 09, 2021
Floating Point	Jan 09, 2024	ACTIVE	Kenneth Rovers	Imagination Technologies			Apr 15, 2022
Functional Safety	Apr 12, 2024	ACTIVE	Daniel Gracia	Thales			Jan 27, 2020
Graphics	Jan 09, 2024	ACTIVE	lakovos Stamoulis	Think Silicon	Abel Bernabeu	Esperanto Technologies Inc.	Jun 07, 2021
HPC	Mar 19, 2024	ACTIVE	Nick Brown	Edinburgh Parallel Computing Centre	Teresa Cervero	Barcelona Supercomputing Center	Jul 23, 2019
Hypervisors	Mar 26, 2024	ACTIVE	Andrew Jones	Ventana Micro Systems	Stefano Stabellini	AMD	Oct 29, 2021
Managed Runtimes	Jan 09, 2024	ACTIVE	Sanhong Li	Alibaba	Ian Rogers	Google	Mar 03, 2021
Microarchitecture Side Channels	Apr 12, 2024	ACTIVE	Ronan Lashermes	INRIA	Allison Randal	Individual Member	Oct 21, 2021
Performance Analysis	Apr 24, 2024	ACTIVE	Beeman Strong	Rivos Inc.	Snehasish Kumar	Google	Apr 04, 2022
Performance Modeling	Apr 10, 2024	ACTIVE	Arup Chakraborty	Ventana Micro Systems	Knute Lingaard	MIPS	Feb 16, 2022
Profiles SIG	Mar 25, 2024	ACTIVE	David Weaver	Akeana	James Ball	Qualcomm	Mar 25, 2024
Quality of Service	Jan 09, 2024	ACTIVE	Ambika Krishnamoo rthy	Intel	Barna Ibrahim	Rivos Inc.	May 06, 2022
RAS (Reliability, Availability, Serviceability)	Jan 09, 2024	ACTIVE	Dr. Ma Xiaohan	Alibaba	Aaron Durbin	Rivos Inc.	Oct 17, 2022
RISC-V Common Software Interface (RVM-CSI)	Jan 09, 2024	ACTIVE	Chris Owen	Imagination Technologies	Vincent Cui	Alibaba	Nov 23, 2023
RVA23/RVB23 Profiles	May 08, 2024	ACTIVE	Greg Favor	Ventana Micro Systems	Krste Asanovic	SiFive	May 08, 2024
Runtime Integrity	Apr 12, 2024	ACTIVE	Nick Kossifidis	FORTH	Deepak Gupta	Rivos Inc.	Nov 17, 2022
Security Response Team	Jan 09, 2024	ACTIVE	Don Bailey	Individual Member			Jan 07, 2021
Simulators	Jan 09, 2024	ACTIVE	Binguang Zhao	Alibaba			Feb 07, 2021
Soft CPU	Apr 30, 2024	ACTIVE	Guy Lemieux	Individual Member	Jan Gray	Individual Member	Nov 08, 2023
Toolchains	Apr 24,	ACTIVE	Christoph	VRULL	Ana Pazos	Rivos Inc.	Aug 31,

Trusted Computing	Apr 12, 2024	ACTIVE	Ravi Sahita	Rivos Inc.			Sept 27, 2021
Vector	Jan 09, 2024	ACTIVE	José Moreira	IBM	Abel Bernabeu	Esperanto Technologies Inc.	Apr 15, 2022

32 issues

Active RISC-V Task Groups (TGs)

Summary	Updated	Status	Chair	Chair	Vice-	Vice-chair	Group
				Affiliation	Chair	Affiliation	Creation
							Date
Advanced Interrupt Architecture (AIA)	Jan 10, 2024	ACTIVE	Anup Patel	Ventana Micro Systems	John Hauser	Individual Member	Jan 22, 2021
Application-Processor Trusted Execution Environment I/O (CoVE-IO)	Jan 10, 2024	ACTIVE	Samuel Ortiz	Rivos Inc.	Jiewen Yao	Intel	Mar 17, 2023
Application-Processor Trusted Execution Environment (CoVE)	Apr 12, 2024	ACTIVE	Ravi Sahita	Rivos Inc.	Guerney D. H. Hunt	IBM	Mar 08, 2022
Boot and Runtime Services (BRS)	Jan 10, 2024	ACTIVE	Aaron Durbin	Rivos Inc.			Jul 06, 2022
Capacity and Bandwidth Controller QoS Register Interface (CBQRI)	Jan 10, 2024	ACTIVE	Eric Shiu	Rivos Inc.	Ambika Krishnamoo rthy	Intel	Sept 06, 2022
Code Size Reduction	Apr 26, 2024	ACTIVE	Tariq Kurd	Codasip	Ibrahim Abu Kharmeh	Huawei	Sept 02, 2020
Control Transfer Records	Apr 12, 2024	ACTIVE	Beeman Strong	Rivos Inc.	Bruce Abledinger	SiFive	Oct 05, 2022
Cryptographic Extensions	Jan 10, 2024	ACTIVE	Richard Newell	Microchip Technology	Ken Dockser	Tenstorrent	
Debug	Jan 11, 2024	ACTIVE	Paul Donahue	Ventana Micro Systems	Tim Newsome	Individual Member	Dec 17, 2019
E-Trace Encapsulation	Jan 10, 2024	ACTIVE	lain Robertson	Siemens	Paul Donahue	Ventana Micro Systems	Jun 01, 2023
External Debug Security	Mar 12, 2024	ACTIVE		NVIDIA			Mar 12, 2024
Fast Interrupts (CLIC)	Apr 12, 2024	ACTIVE	Dan Smathers	Seagate	Jean- Baptiste Brelot	Nordic Semi	Feb 18, 2022
High Assurance Cryptography (HAC)	Feb 02, 2024	ACTIVE	Richard Newell	Microchip Technology	Markku Saarinen	Tampere University	Aug 14, 2024
I/O Physical Memory Protection Unit (IOPMP)	Apr 12, 2024	ACTIVE	Paul Ku	Andes	Channing Tang	NVIDIA	Feb 09, 2022
Input-Output Memory Management Unit (IOMMU)	Jan 10, 2024	ACTIVE	Vedvyas Shanbhog ue	Rivos Inc.	Perrine Peresse	SiFive	Nov 16, 2021
Nexus	Apr 12, 2024	ACTIVE	Robert Chyla	MIPS	Jay Gamoneda	NXP	Jun 07, 2019
Packed Single Instruction, Multiple Data (SIMD)	May 01, 2024	ACTIVE	Kevin Chen	Andes	Wei Wu	ISCAS	Dec 17, 2019
Platform Management Interface (RPMI)	Mar 05, 2024	ACTIVE	Leyfoon Tan	StarFive	Rahul Pathak	Ventana Micro Systems	Mar 01, 2024

Platform Runtime Services	Apr 10, 2024	ACTIVE	Atish Patra	Rivos Inc.	Sunil VL	Ventana Micro Systems	Oct 08, 2022
Pointer Masking (J)	Apr 12, 2024	ACTIVE	Martin Maas	Google			Dec 17, 2019
Post-Quantum Cryptography (PQC)	Feb 02, 2024	ACTIVE	Markku Saarinen	Tampere University	Nicolas Brunie	SiFive	Mar 27, 2023
RAS Error-record Register Interface (RERI)	Jan 10, 2024	ACTIVE	Greg Favor	Ventana Micro Systems	Ved Shanbhogu e	Rivos Inc.	Jun 07, 2023
RVV C Intrinsic API	Jan 10, 2024	ACTIVE	Eop Chen	NVIDIA	Roger Ferrer Ibanez	Barcelona Supercomputing Center	Oct 12, 2022
S-mode Physical Memory Protection (SPMP)	Apr 12, 2024	ACTIVE	Dong Du	Shanghai Jiao Tong University			Feb 18, 2022
Security Model	Apr 12, 2024	ACTIVE	Paul Elliot	Codasip			Apr 29, 2022
Server Platforms	Apr 27, 2024	ACTIVE	Andrei Warkentin	Intel	Andrew Jones	Ventana Micro Systems	Apr 22, 2024
Server SOC	Jan 10, 2024	ACTIVE	Ved Shanbhog ue	Rivos Inc.	Shaolin Xie	Alibaba	May 04, 2023
Shadow Stack & Landing Pads	Apr 12, 2024	ACTIVE	Giorgos Christou	Technical University of Crete	Ved Shanbhogu e	Rivos Inc.	Jun 13, 2022
SmMTT (Supervisor Domain Isolation)	Feb 23, 2024	ACTIVE	Ravi Sahita	Rivos Inc.	Krste Asanovic	SiFive	Jun 15, 2023
Unified Discovery	Jan 10, 2024	ACTIVE	Dr. Siqi Zhao	Alibaba	Brad Friedman	Intel	Apr 07, 2020
Vector Extension 2	Jan 10, 2024	ACTIVE	Krste Asanovi	SiFive	Roger Espasa	Semidynamics	Dec 17, 2019

31 issues

Distribution of Chairs base on Affiliation

	Chair Aff	iliation										
Group Type	Akeana	Alibaba	Andes	Beijing Open Source Chip Research Institute	Bluespec	CloudBEAR	Codasip	Codeplay	Edinburgh Parallel Computing Centre	FORTH	Google	Huawei
Gover nance Commi ttee (GC)	0	0	0	0	0	0	0	0	0	0	0	0
Horizo ntal Commi ttee (HC)	0	0	0	0	0	0	0	0	0	0	0	1
ISA Commi ttee (IC)	0	0	0	0	0	0	0	0	0	0	0	0
Specia I Interes t Group (SIG)	1	7	0	1	0	0	0	1	1	2	2	0
Task Group (TG)	0	2	2	0	1	1	2	0	0	0	2	0
Irrelev ant	0	0	0	0	0	0	0	0	0	0	0	0
Total Uniqu e Issues	1	9	2	1	1	1	2	1	1	2	4	1

Showing 6 of 6 statistics.

Distribution of Vice-Chairs base on Affiliation

	Vice-chair /	Affiliatio	on										
Group Type	10X Engineers	AMD	Aitos	Alibaba	Aril Inc.	Barcelona Supercomputing Center	ETH Zurich	Esperanto Technologies Inc.	Google	Huawei	IBM	ISCAS	Individua Member
Gover nance Commi ttee (GC)	0	0	0	0	0	0	0	0	0	0	0	0	0
Horizo ntal Commi ttee (HC)	0	0	0	1	0	0	0	1	0	0	0	0	1
ISA Commi ttee (IC)	0	0	0	0	1	0	0	0	0	0	0	0	0
Specia I Interes t Group (SIG)	1	1	1	3	0	1	0	2	3	0	0	0	2
Task Group (TG)	0	0	0	3	0	1	1	0	0	1	2	1	3
Irrelev ant	0	0	0	0	0	0	0	0	0	0	0	0	0
Total Uniqu e Issues :	1	1	1	7	1	2	1	3	3	1	2	1	6

Showing 6 of 6 statistics.